

Amendment "A" page 2 of 14
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In the Specification:

Replace the Abstract beginning on page 38, line 3 as follows:

--A method of noise analysis and correction of noise violations for an integrated circuit design includes steps of ~~[[a]]~~ receiving as input a standard parasitic exchange file for an integrated circuit design and ~~[[; (b)]]~~ parsing the standard parasitic exchange file to generate a resistance graph. A ~~, (c) generating a representation of the resistance graph~~ is generated to determine noise critical nets. A ~~; (d) generating a list~~ is generated of only noise critical nets from the representation of the resistance graph. A ~~, (e) selecting a net~~ is selected from the list of only noise critical nets, and ~~, (f) calculating a value of total crosstalk noise in the selected net from all aggressor nets relative to the selected net~~ is calculated. The ~~, and (g) generating as output the value of total crosstalk noise in the selected net~~ is generated as output for correcting a noise violation.--